

図1

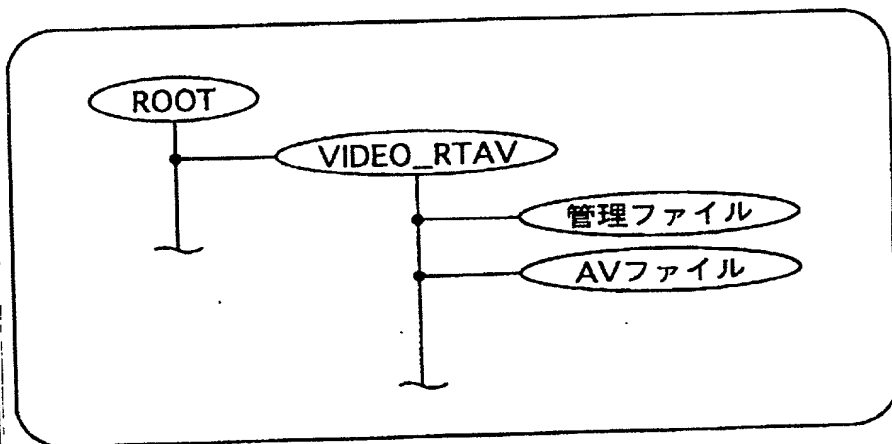
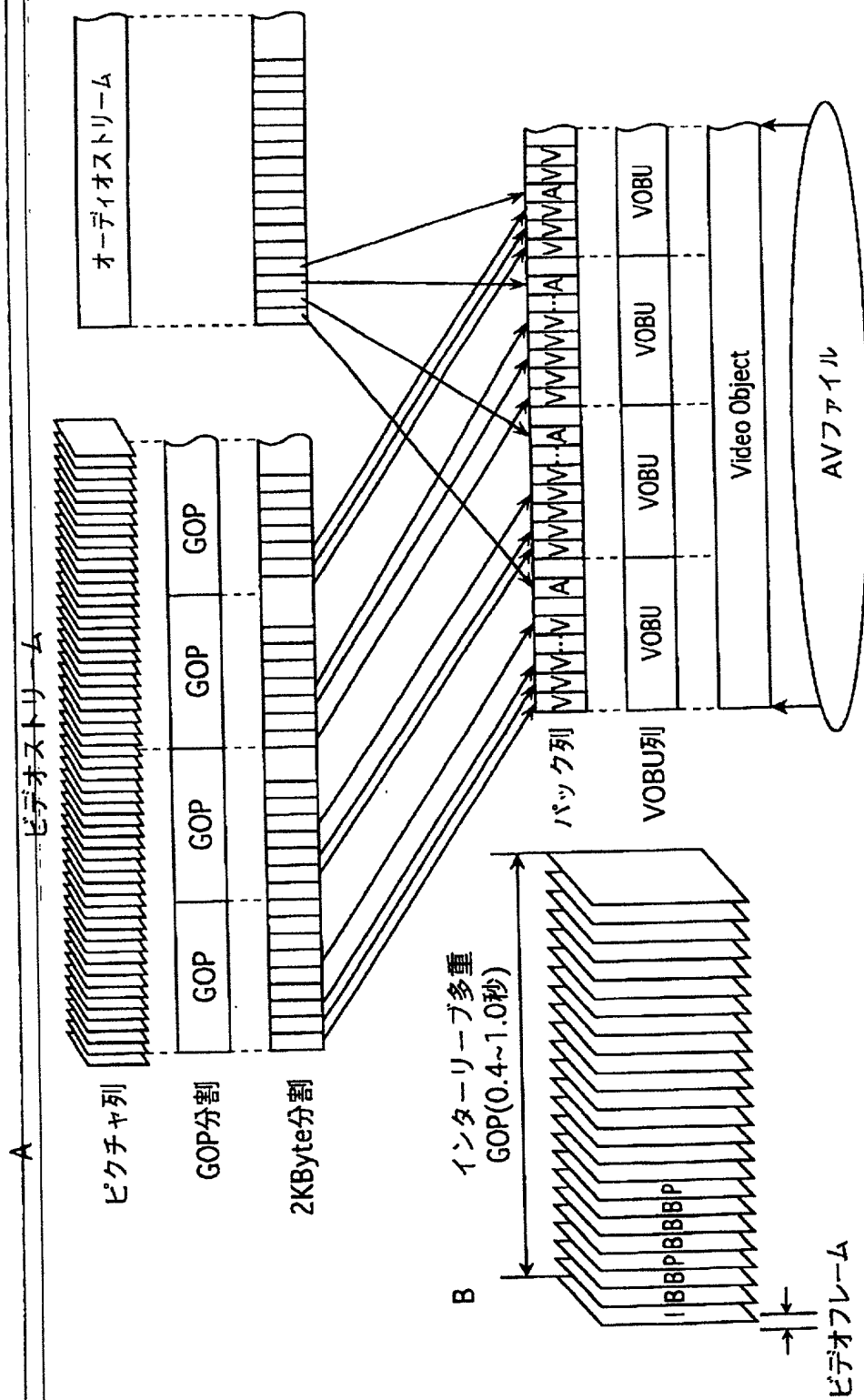
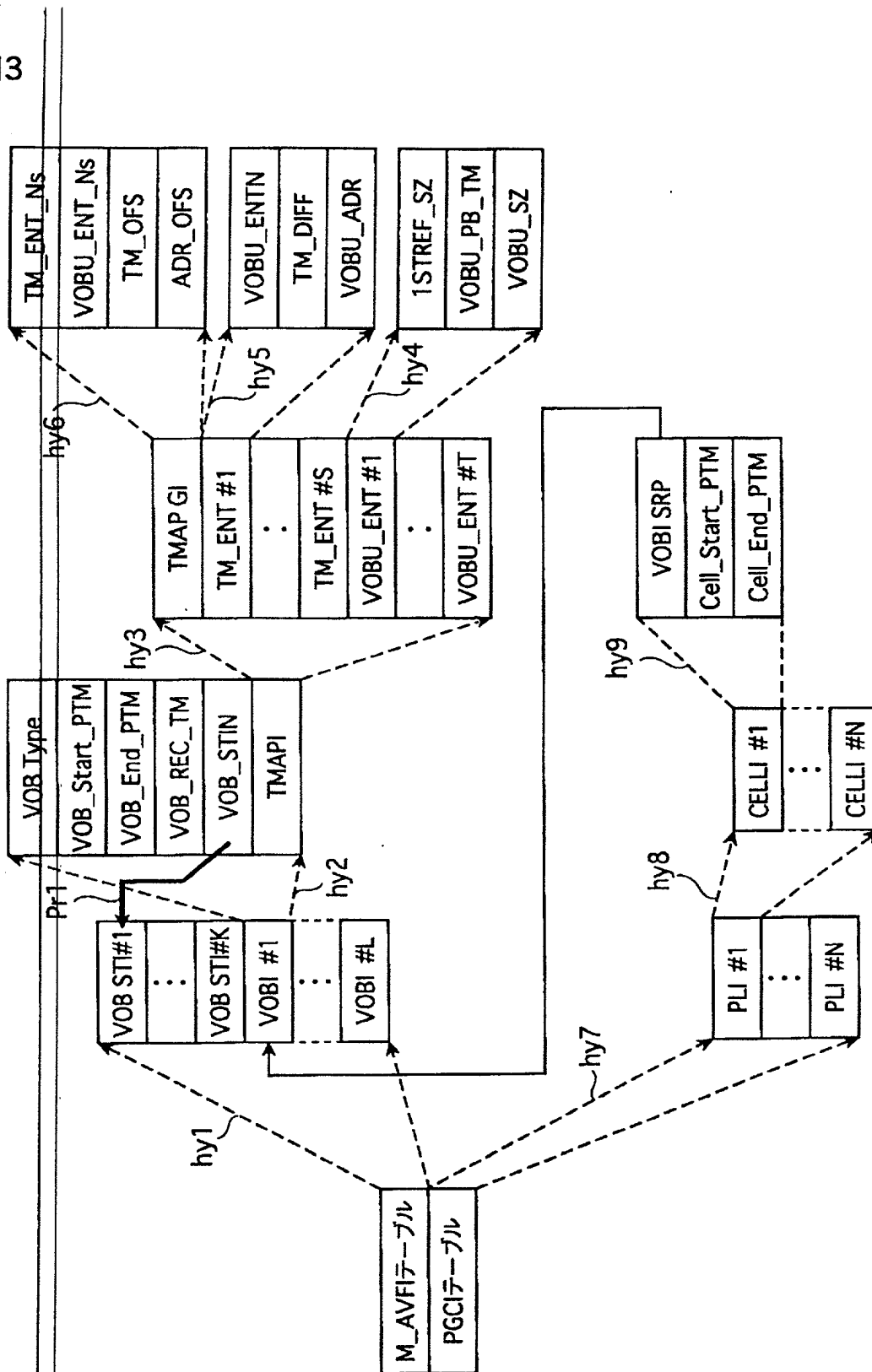


図2





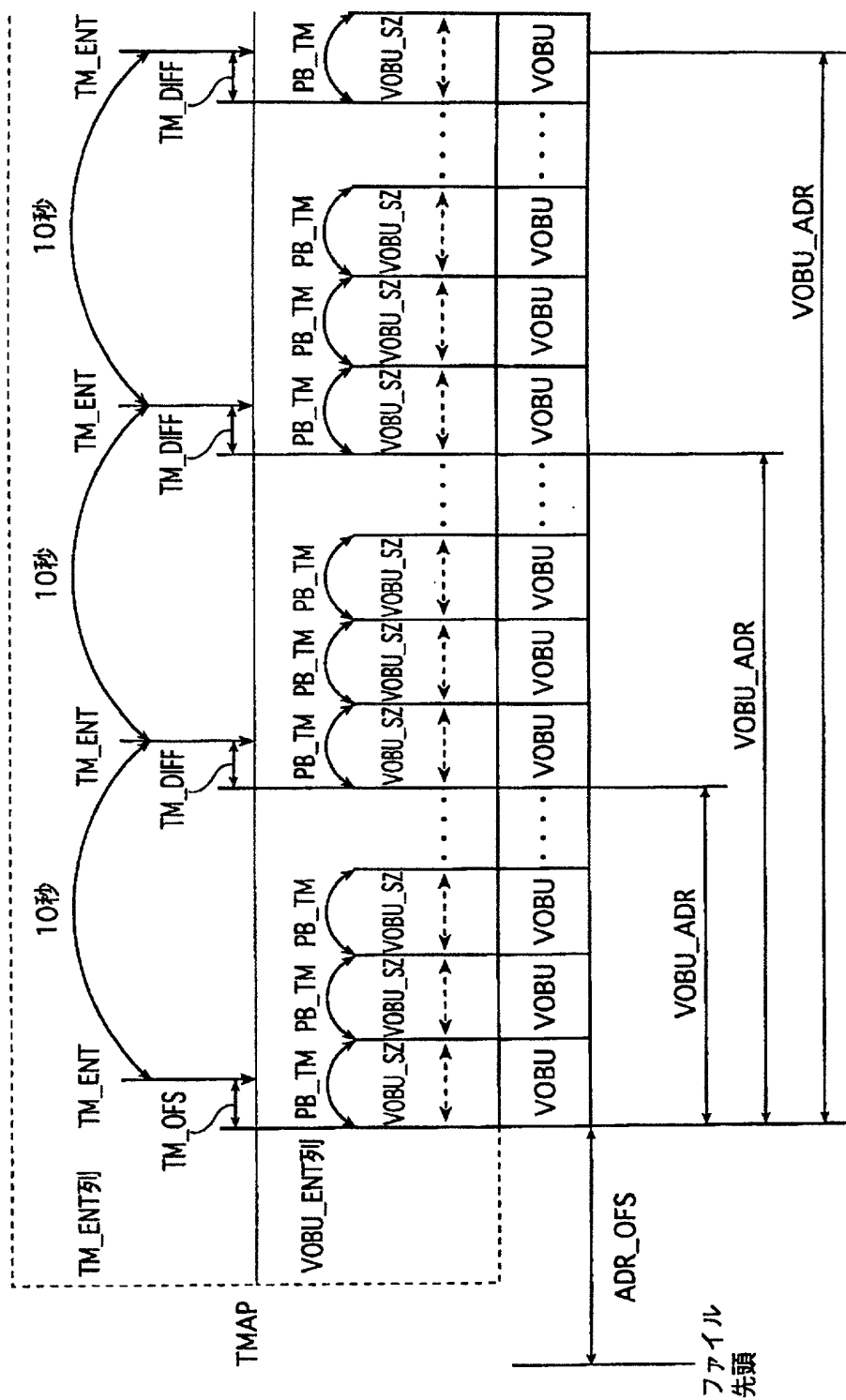


図5

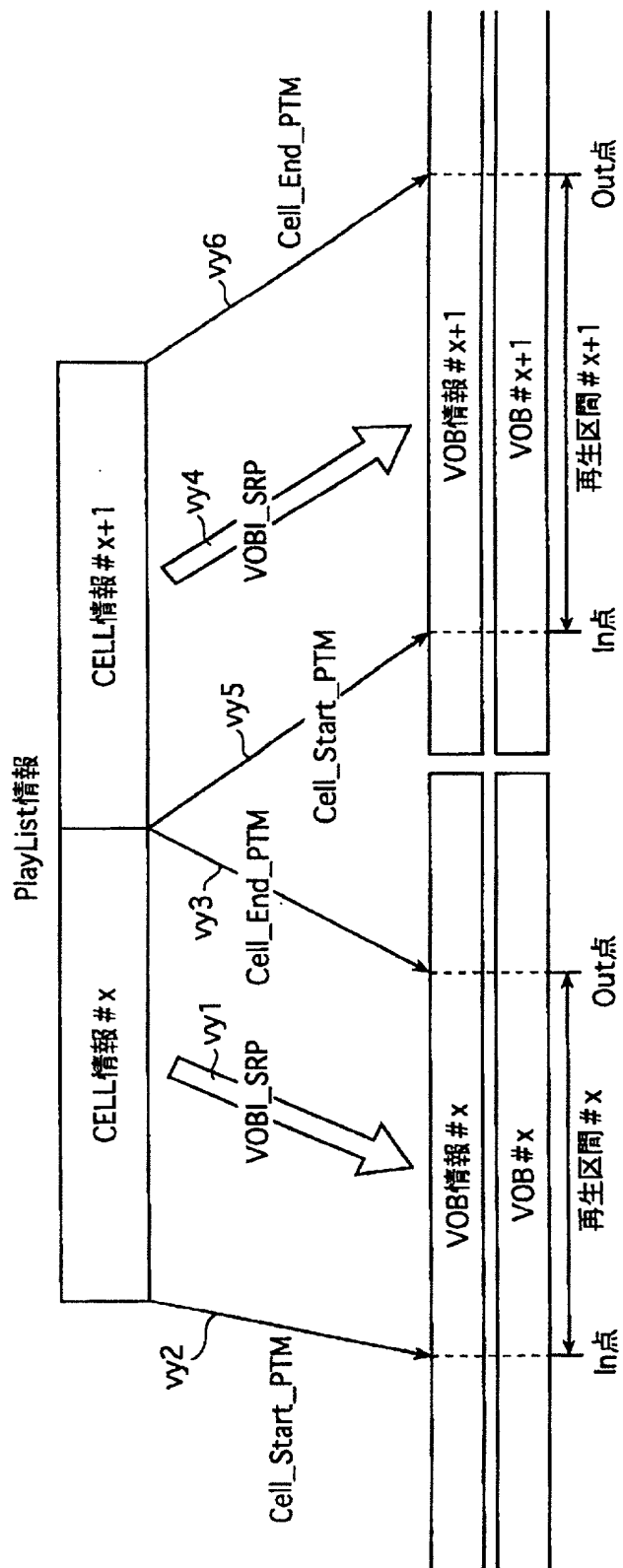


図6

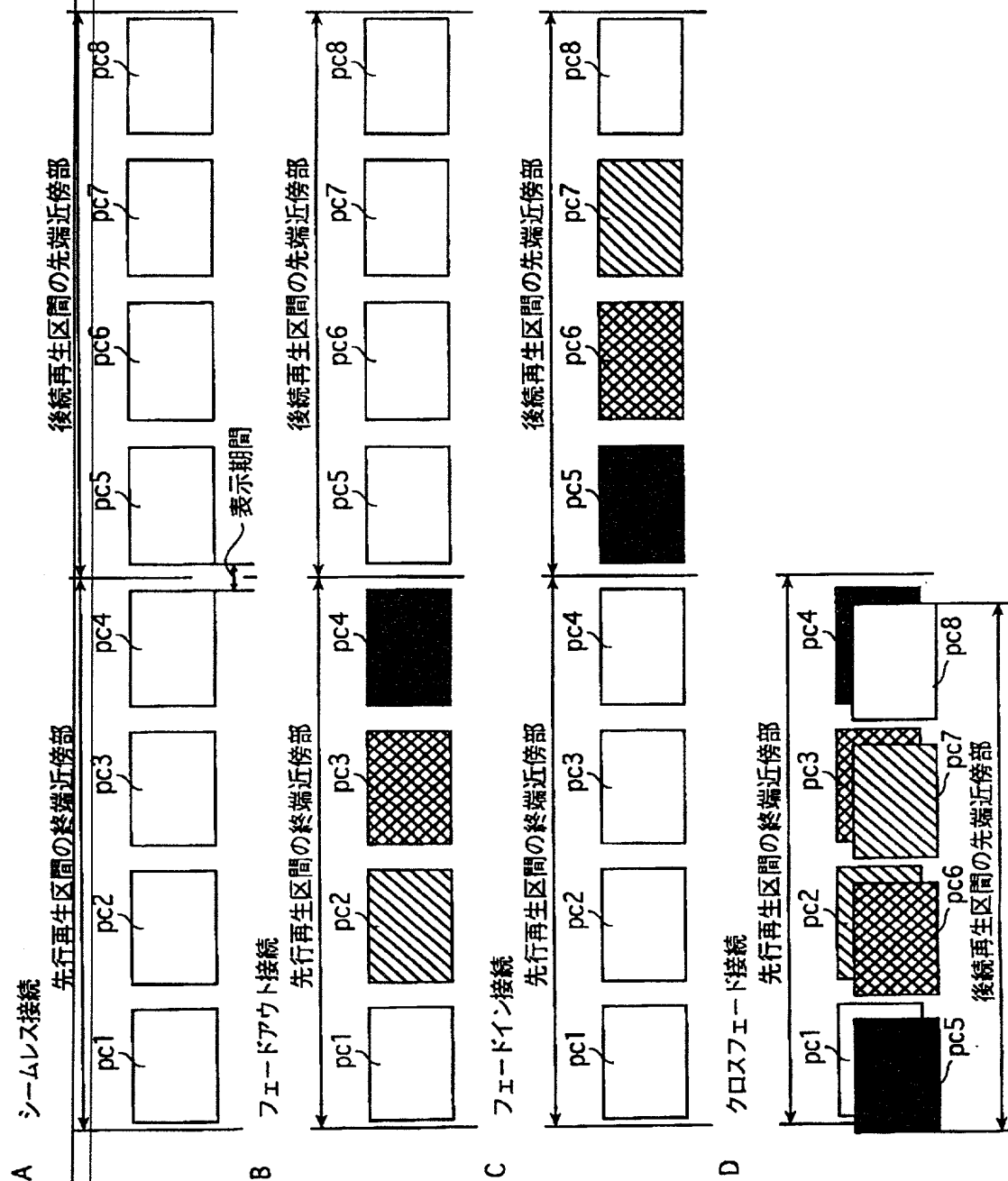
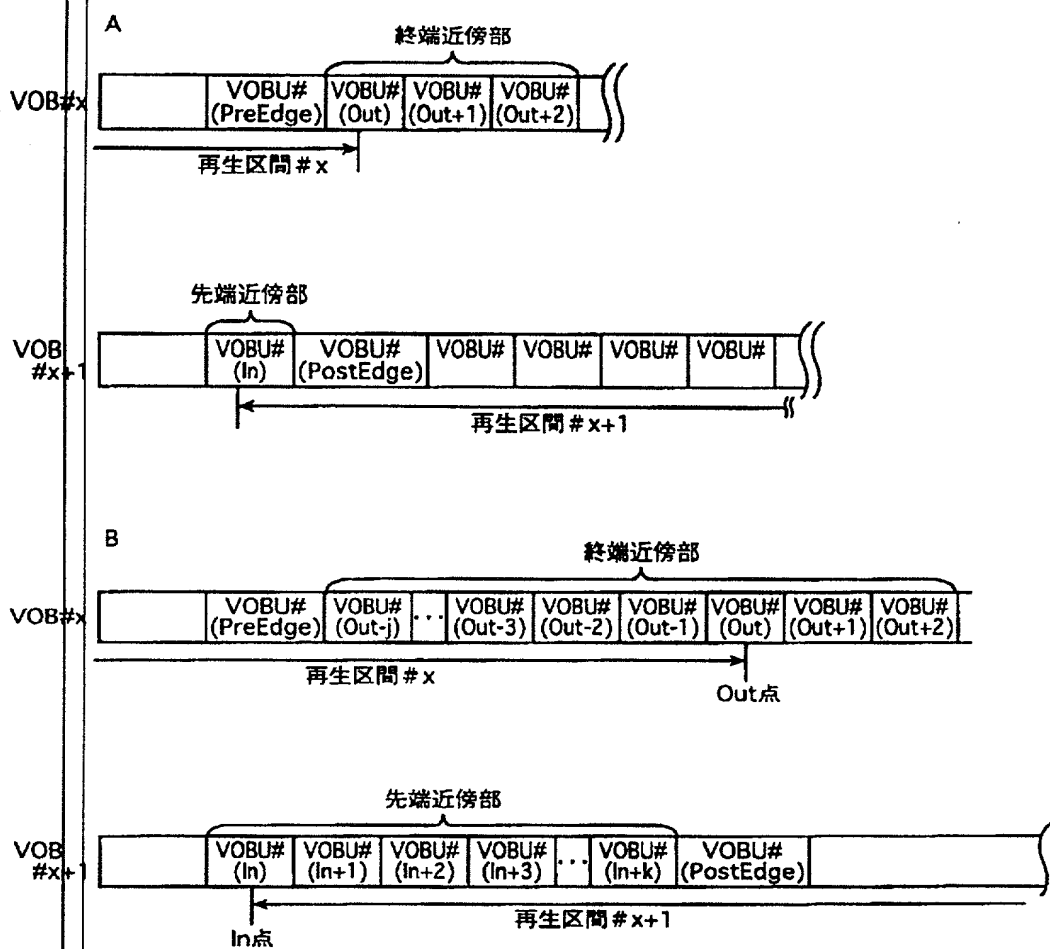


図7



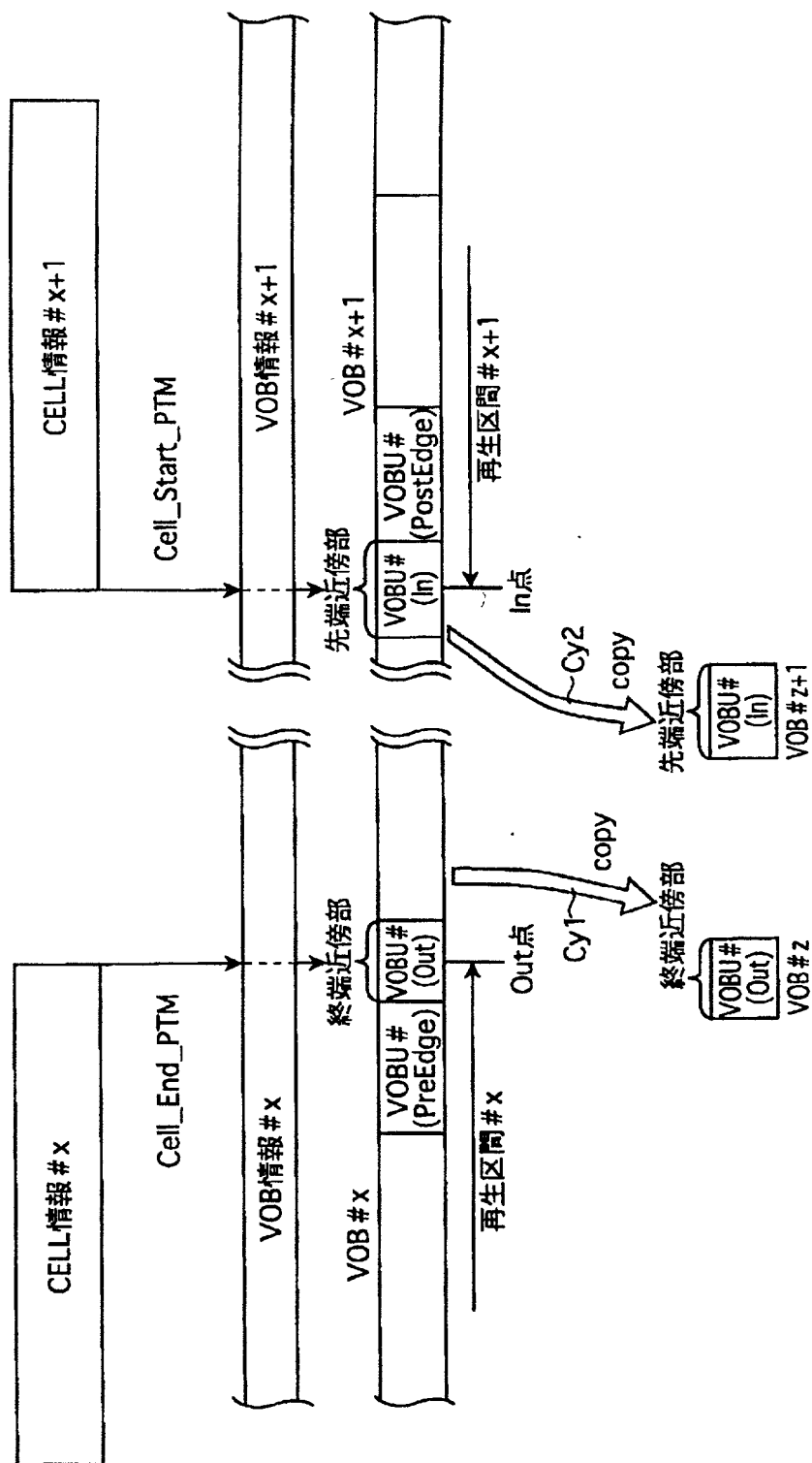


図9

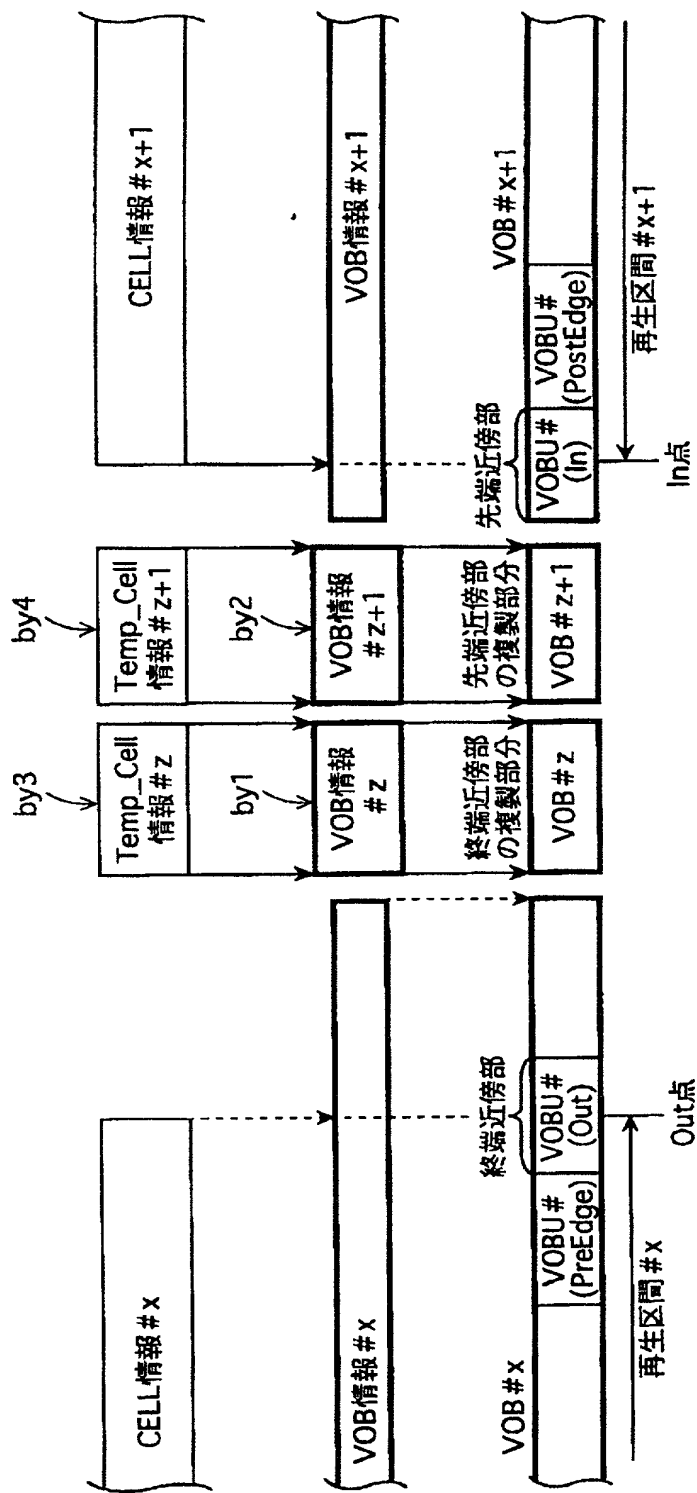
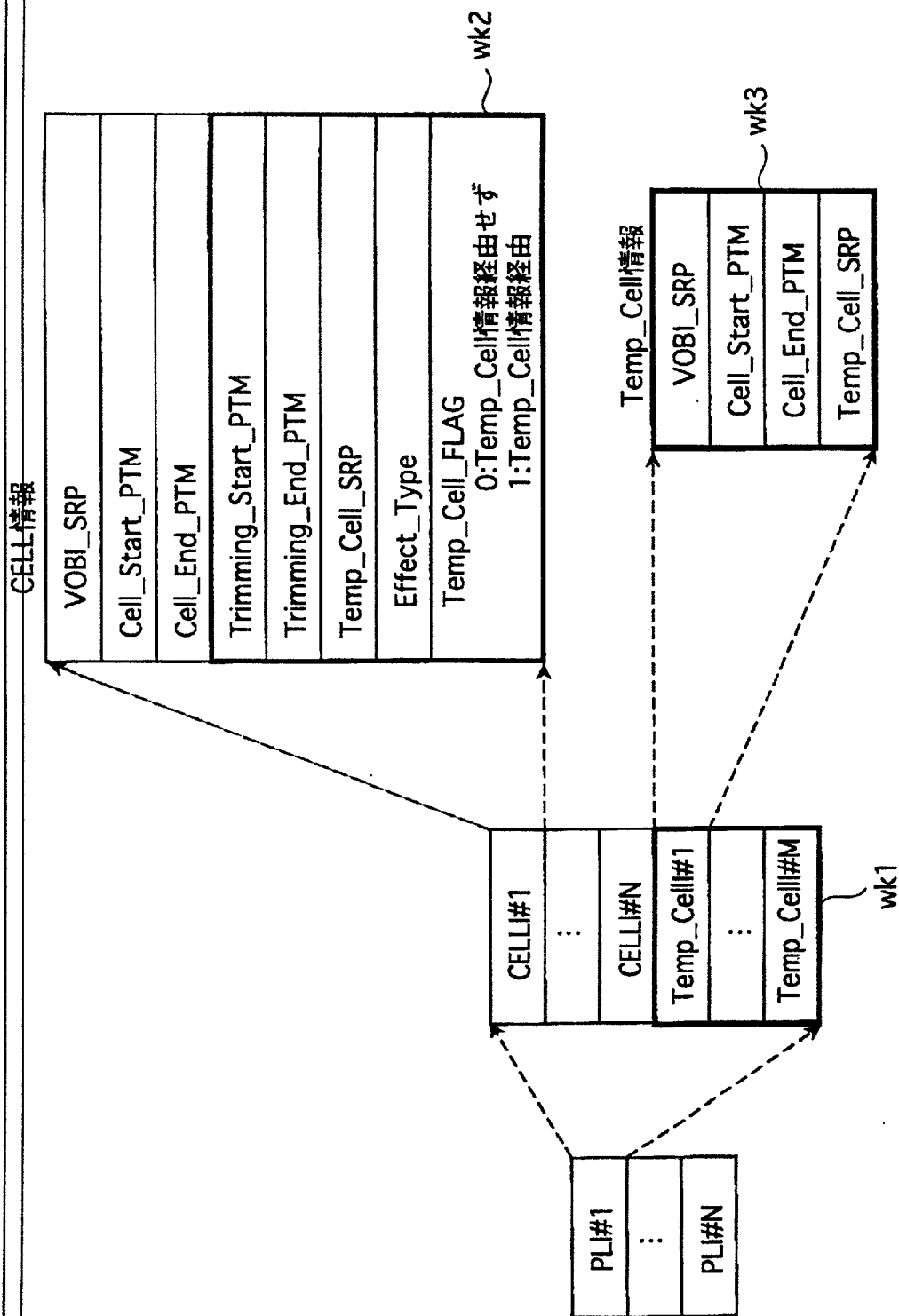
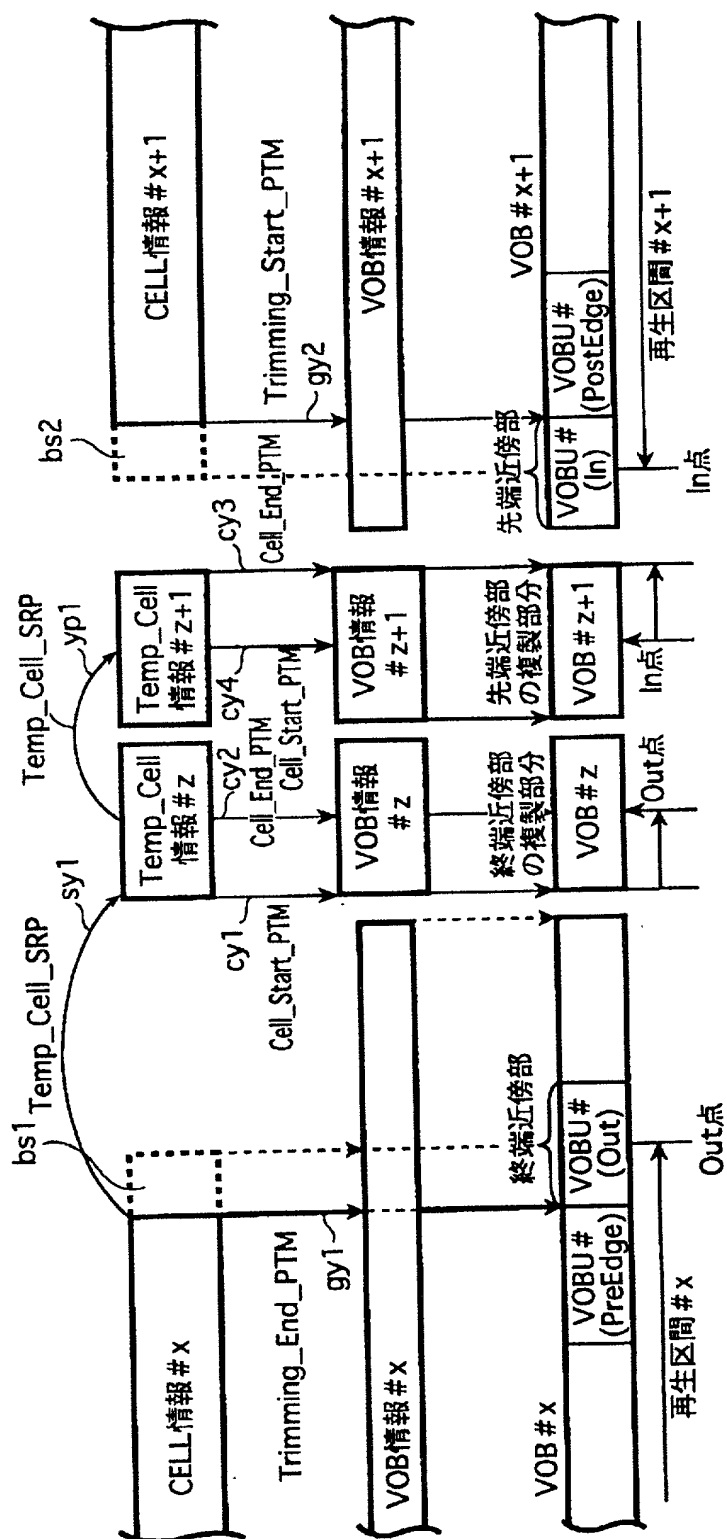


図10





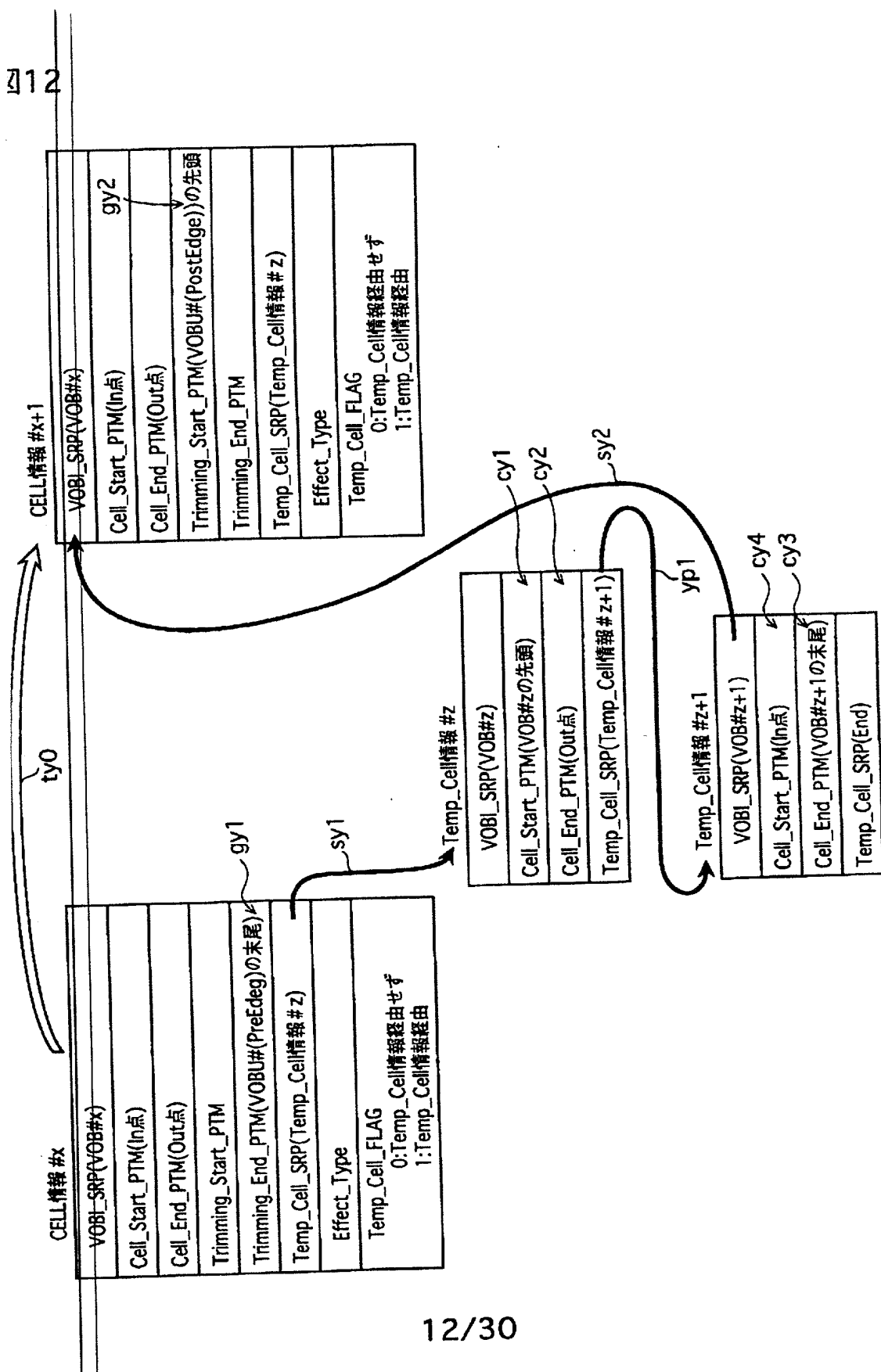
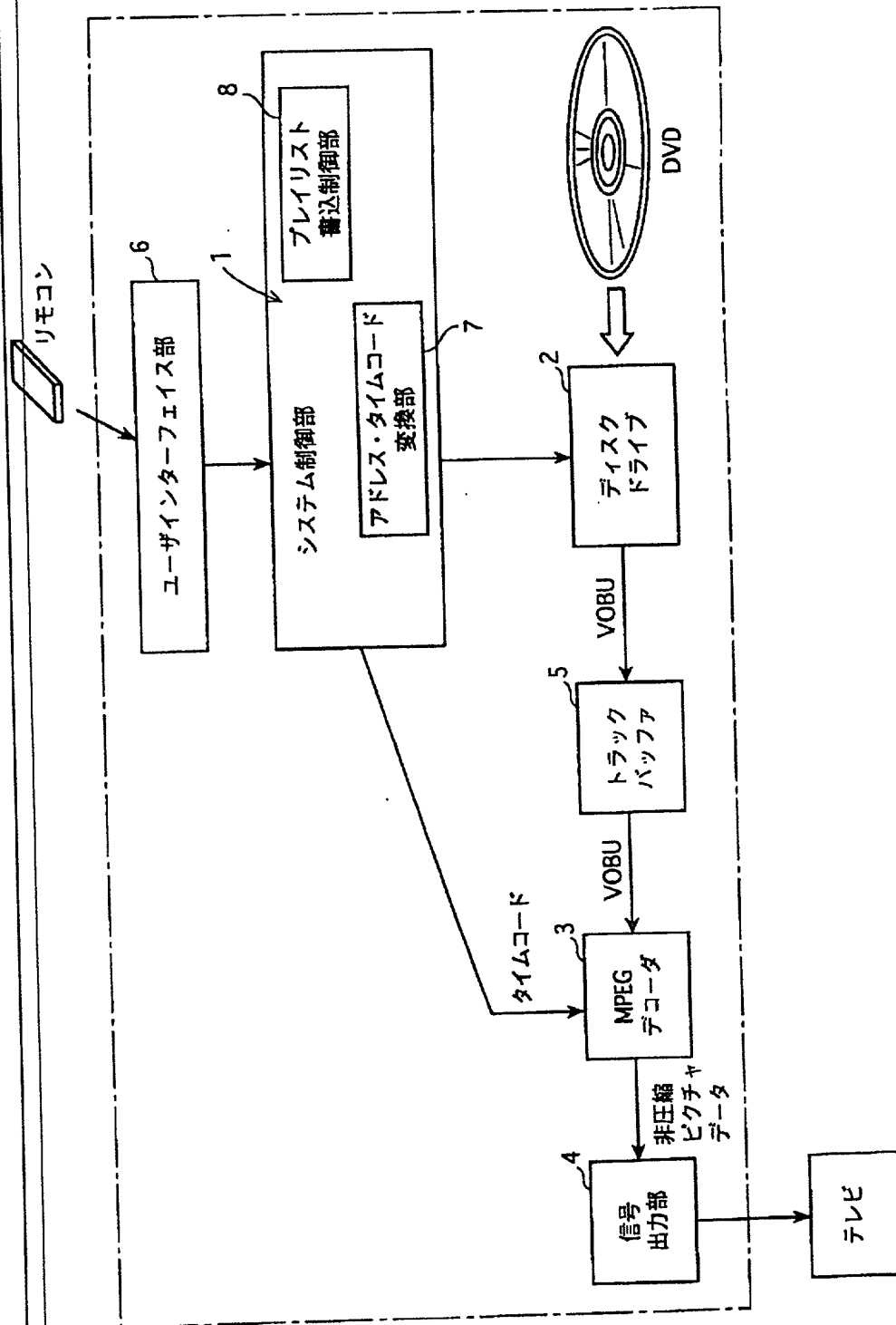
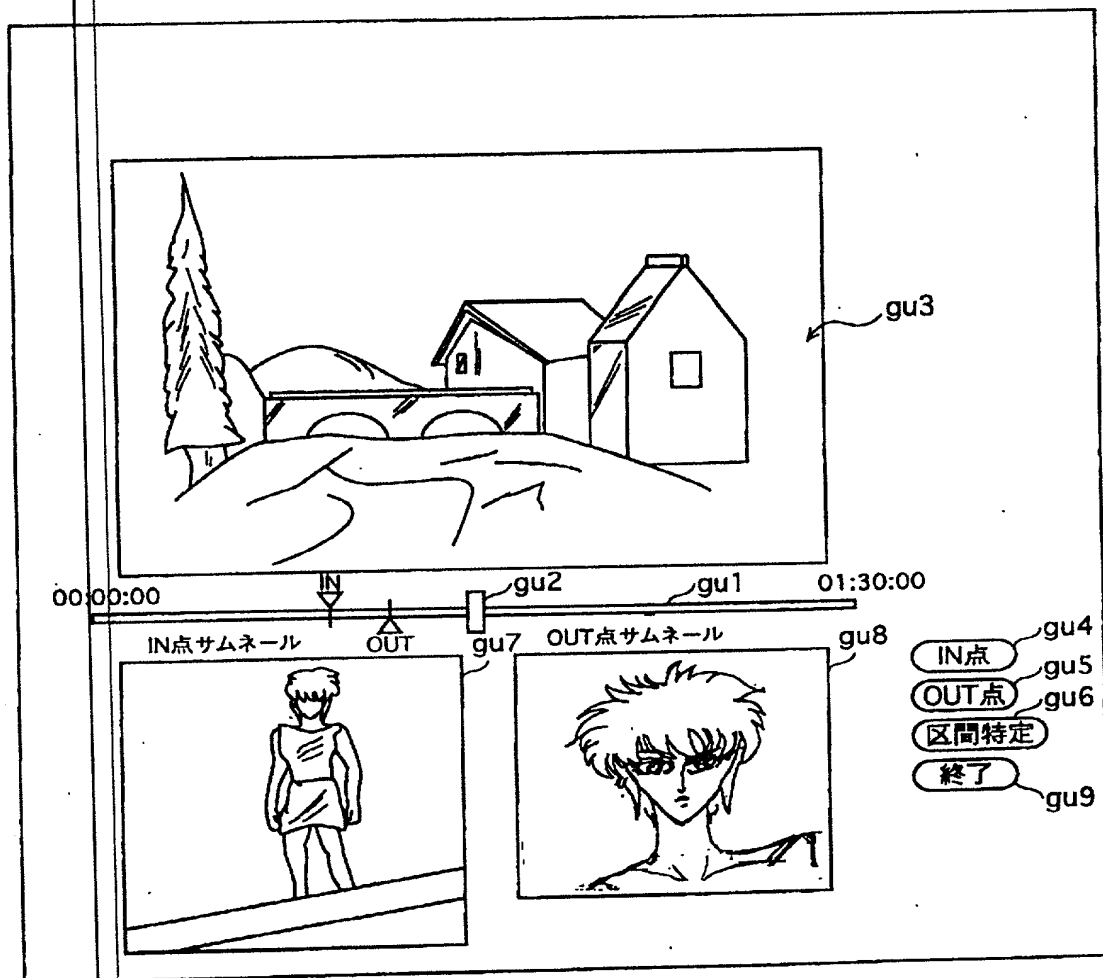
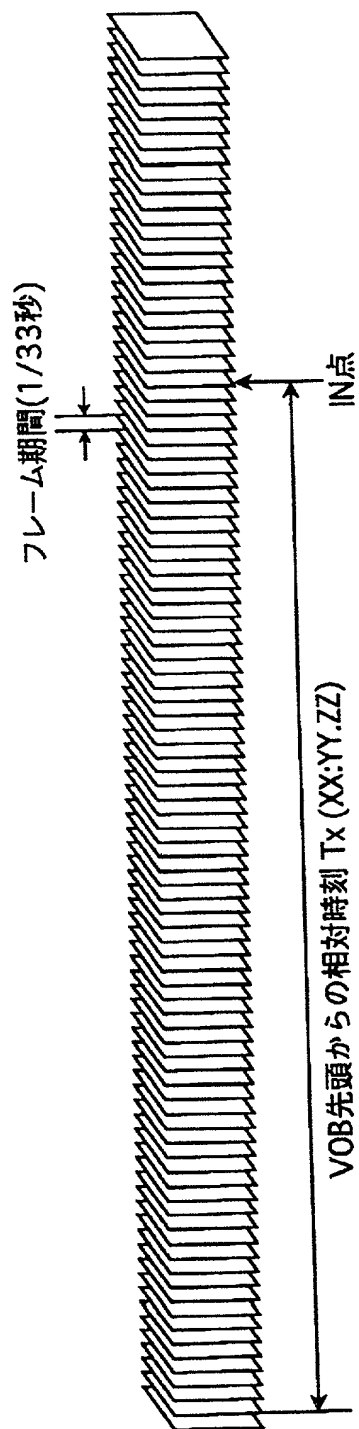
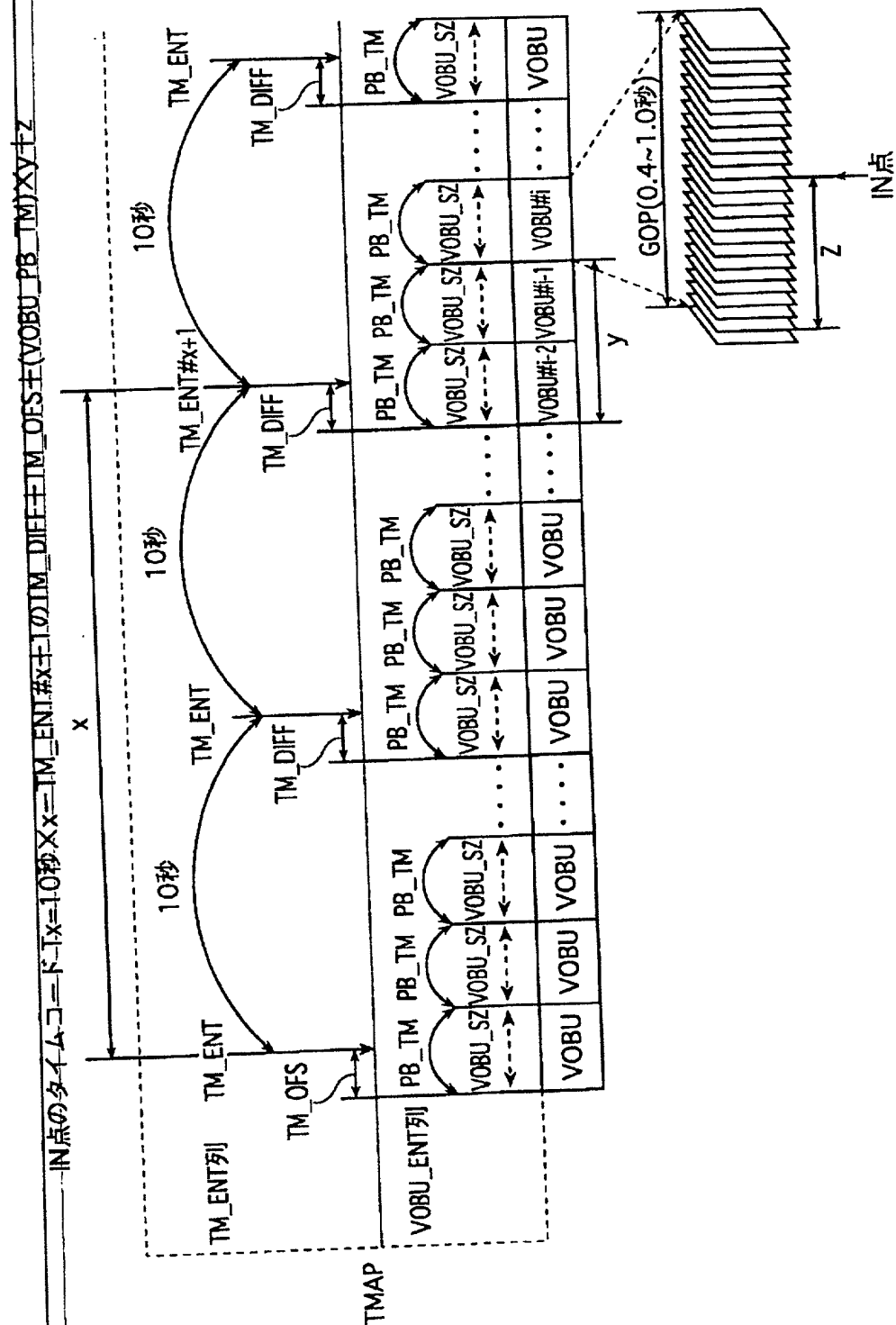


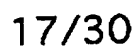
図13











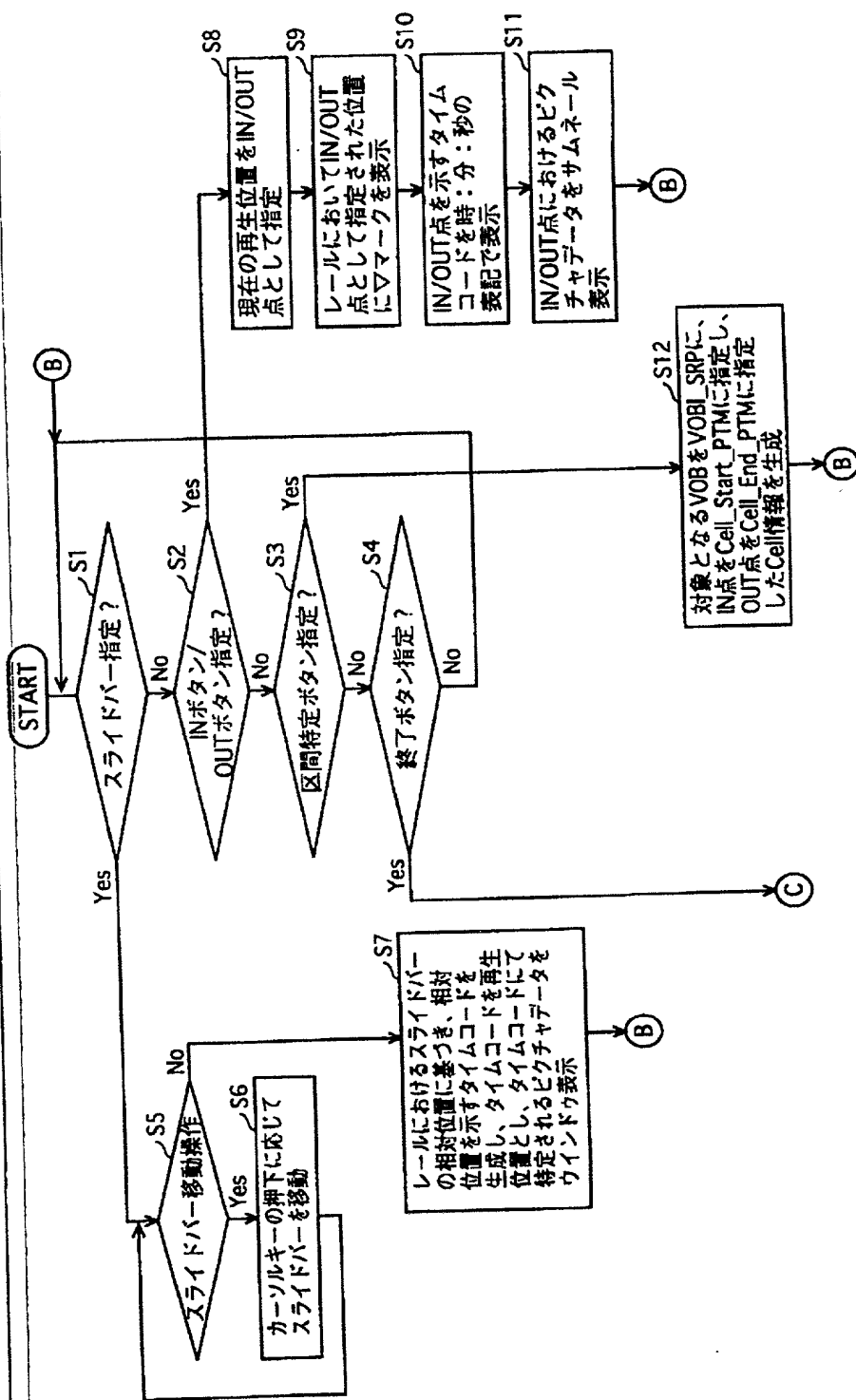
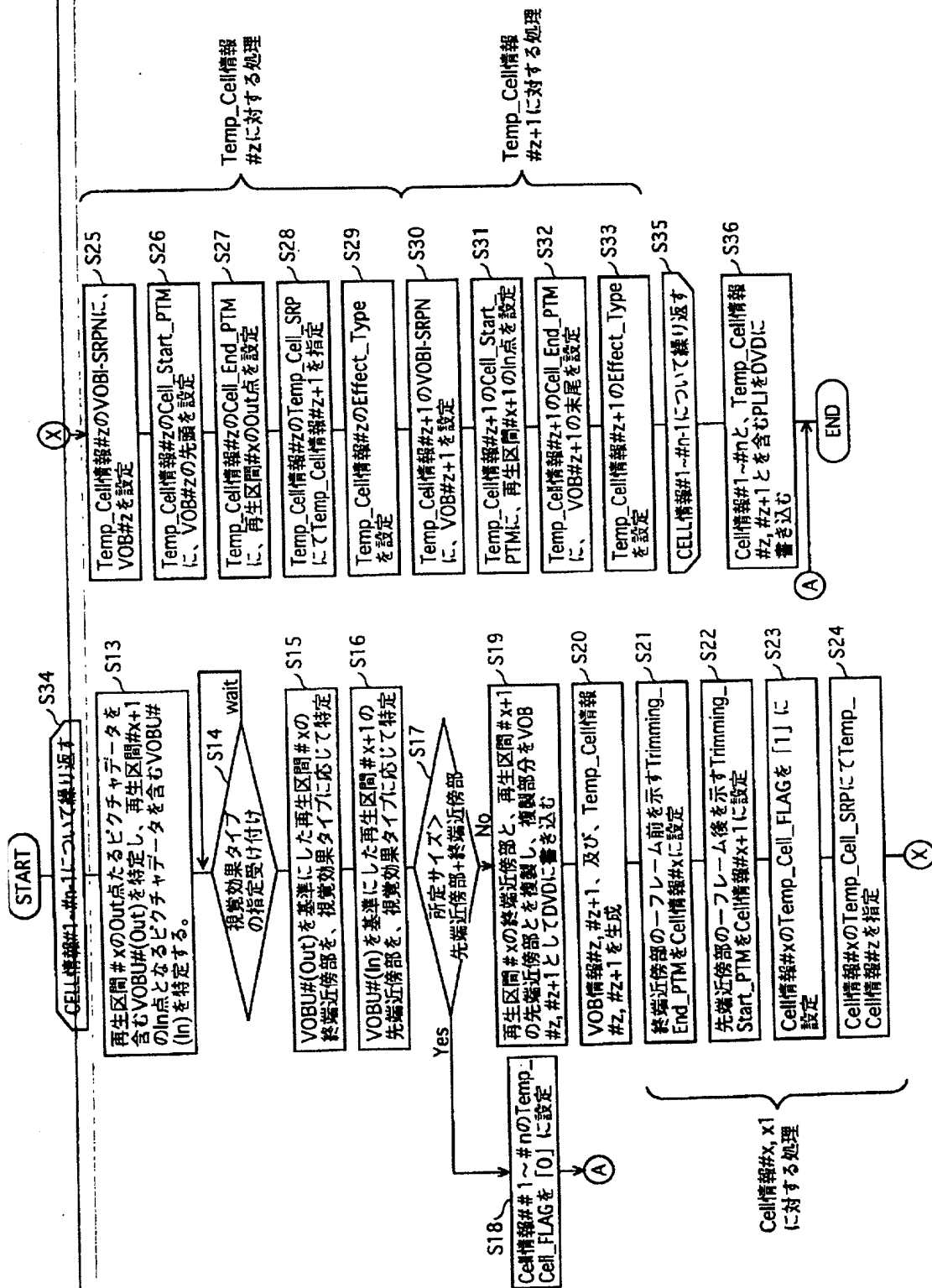


FIG. 19



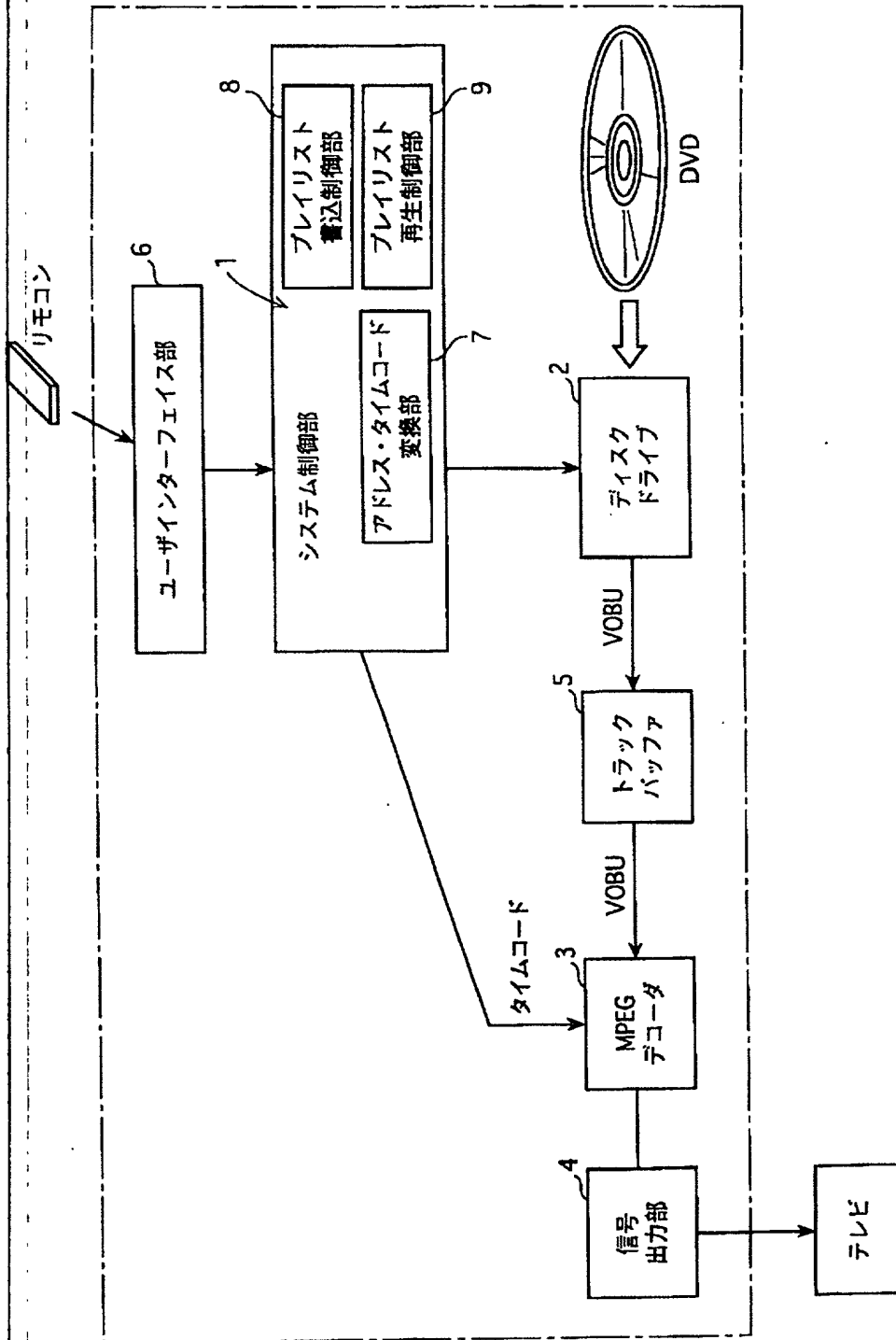
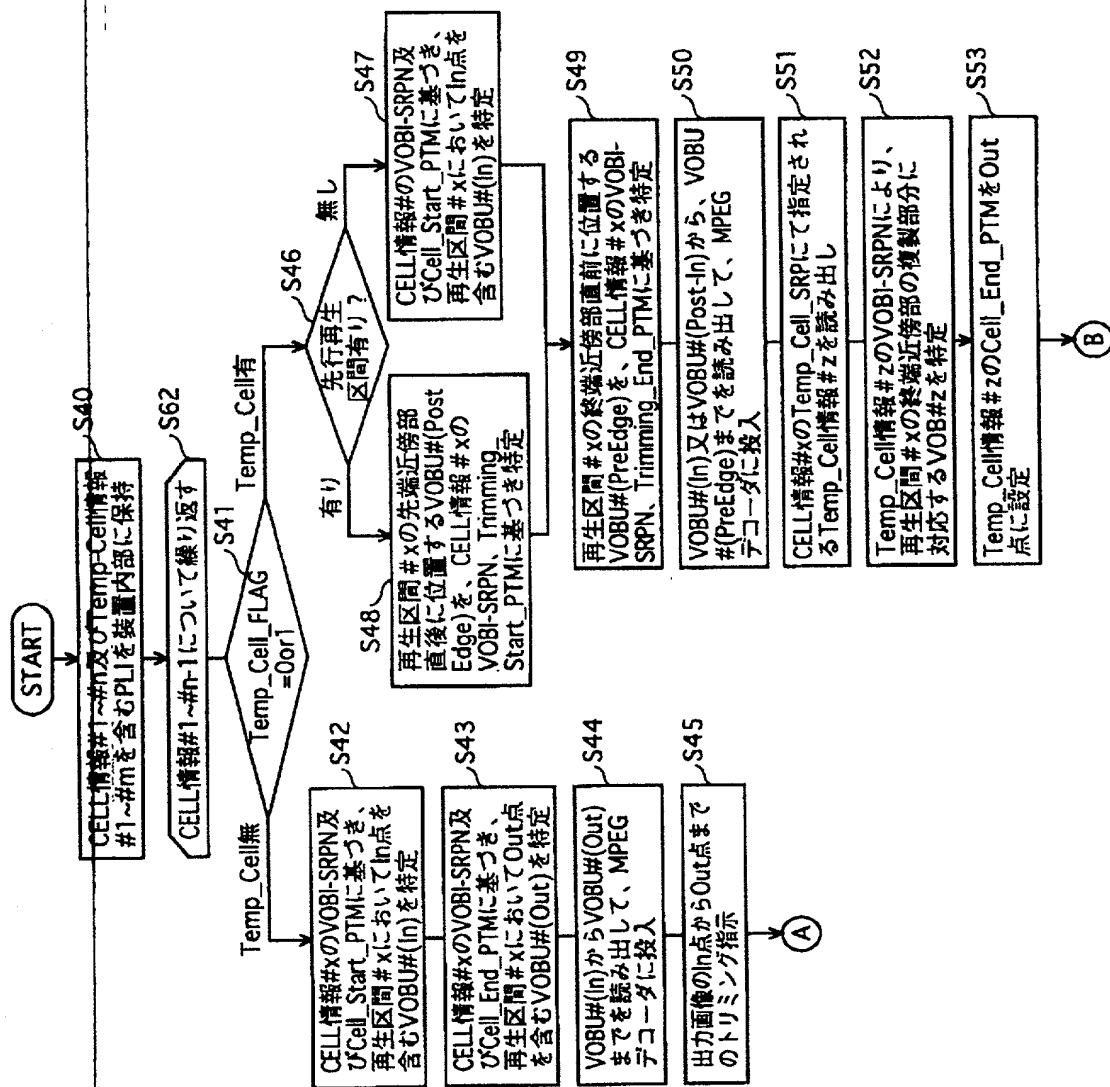
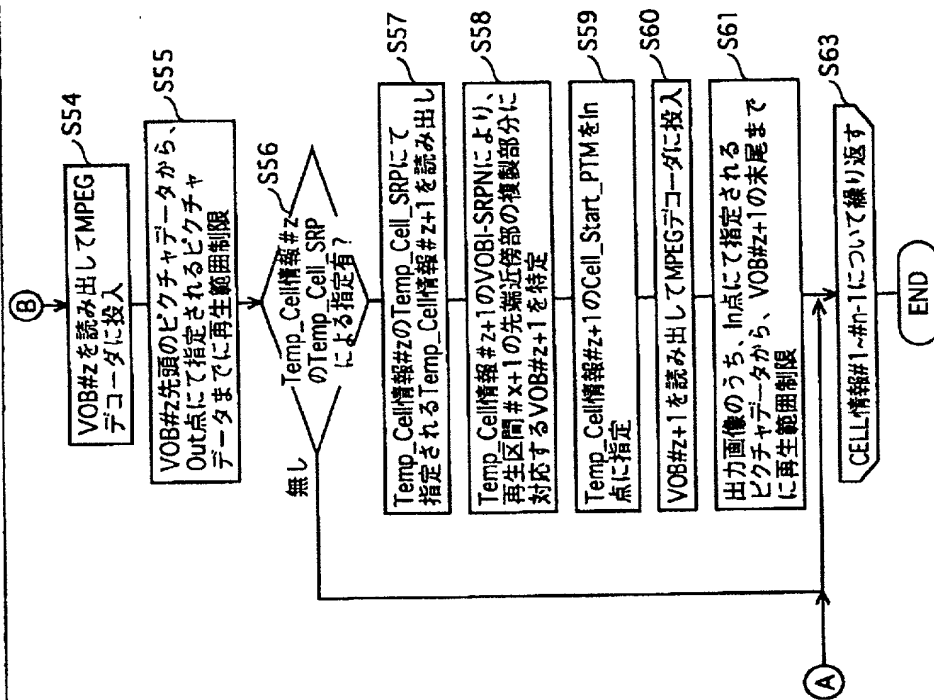


図21





・Temp_Cell_FLAG [0] Temp_Cell情報経由せず

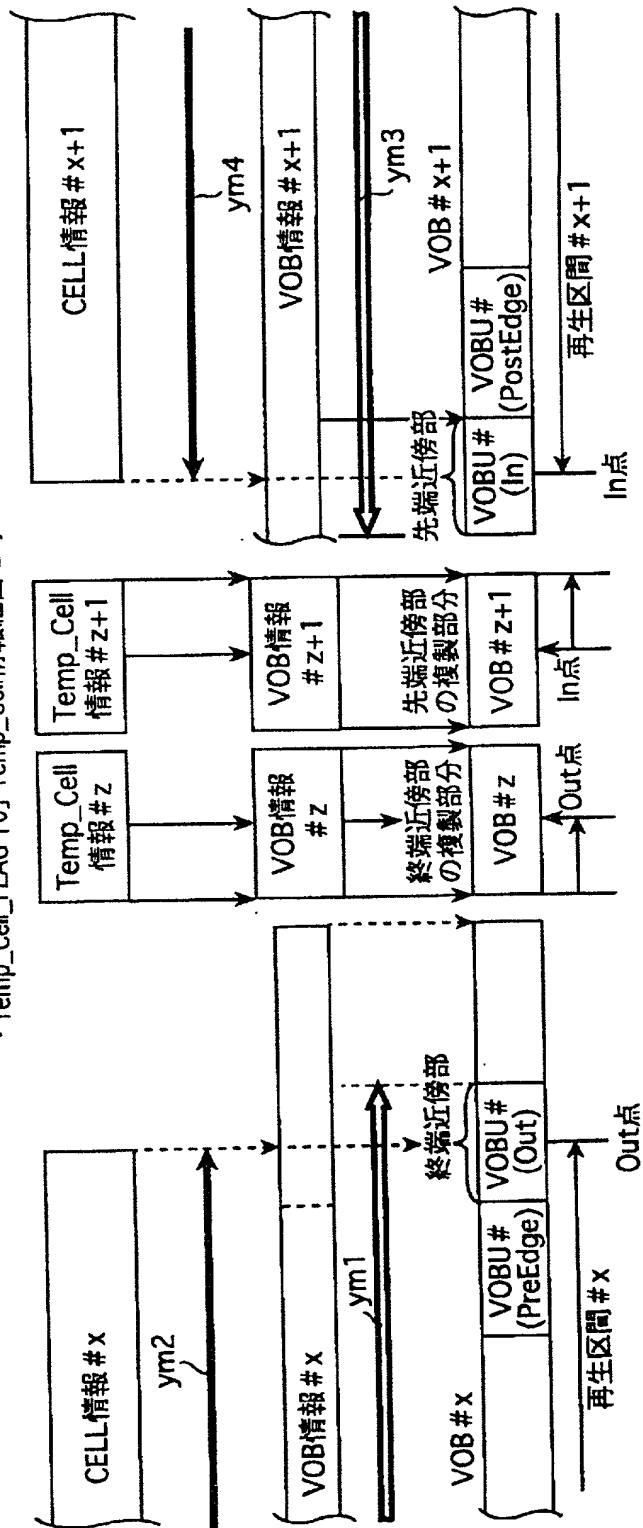


図24

・Temp_Cell_FLAG [1] Temp_Cell情報経由する

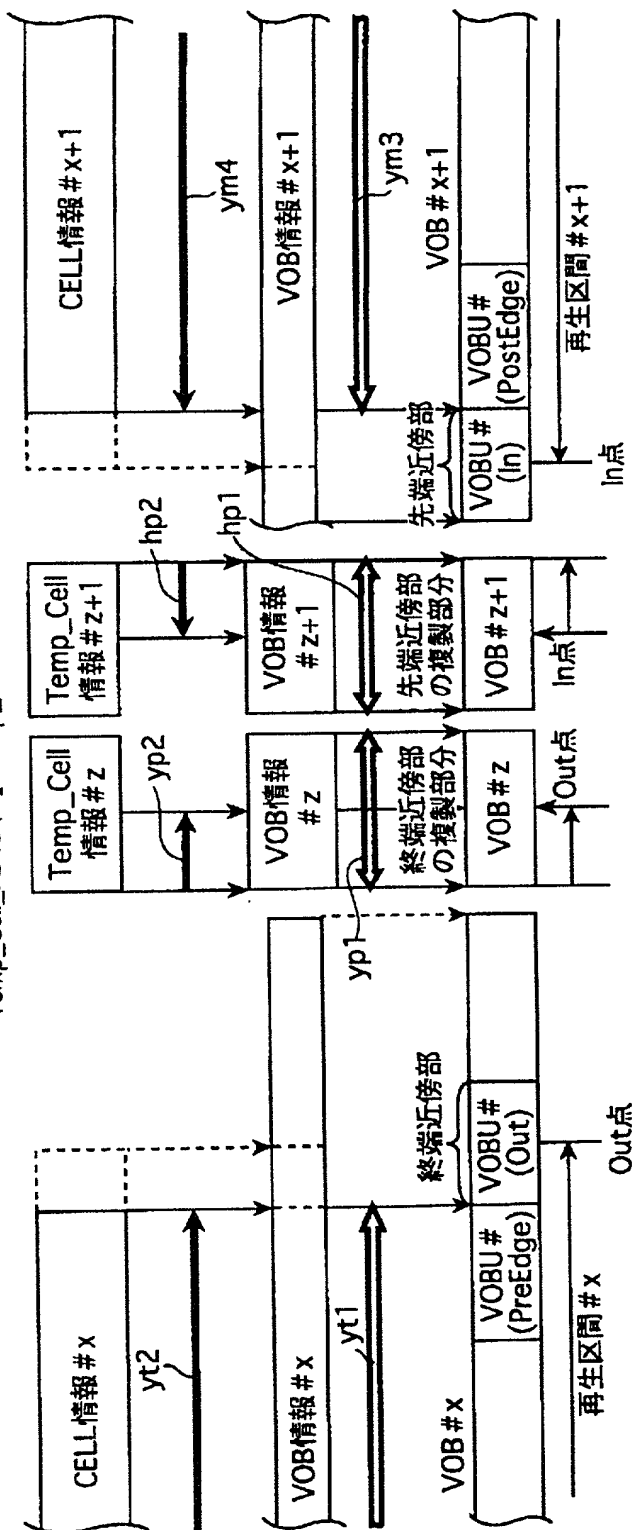
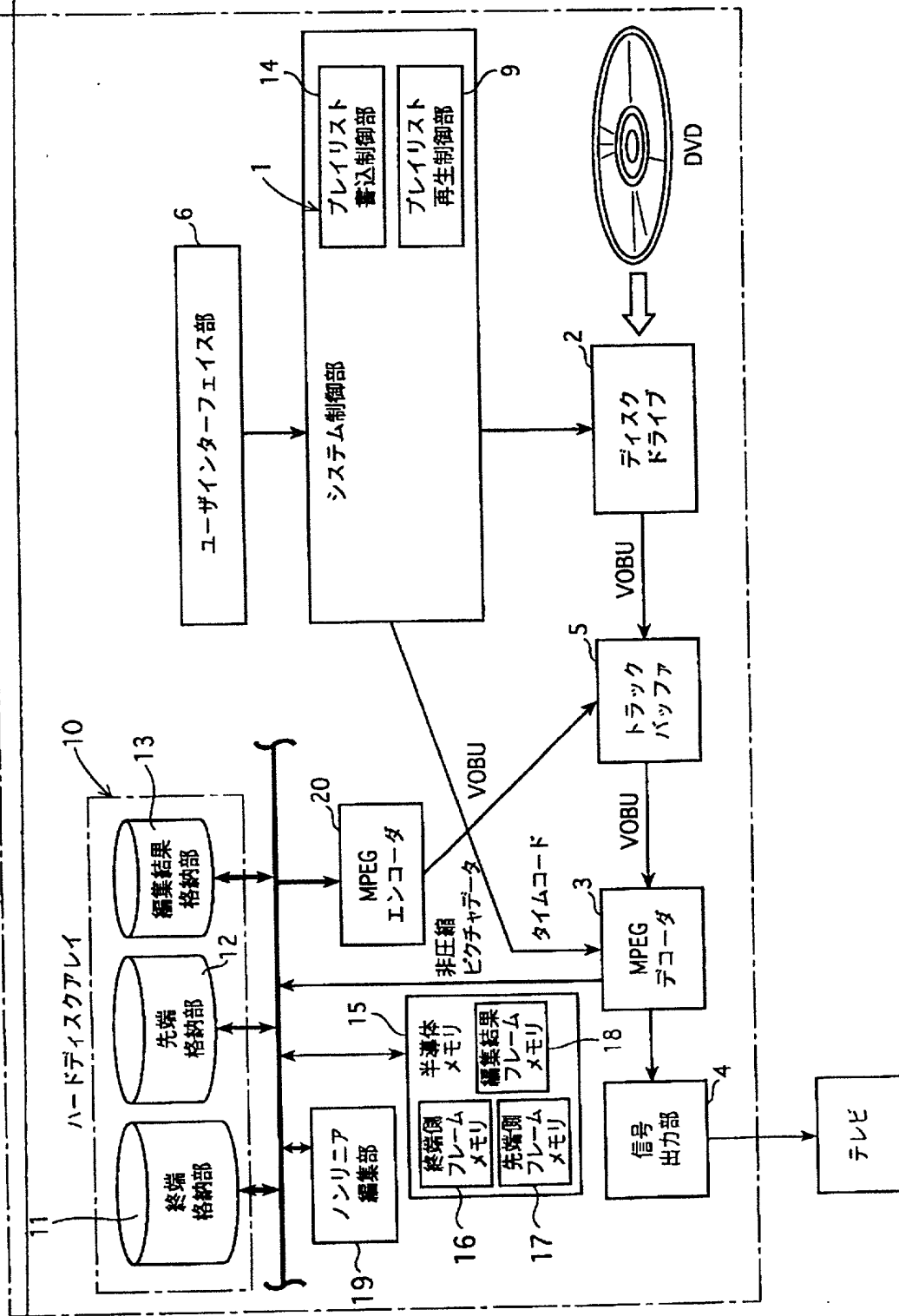


図25



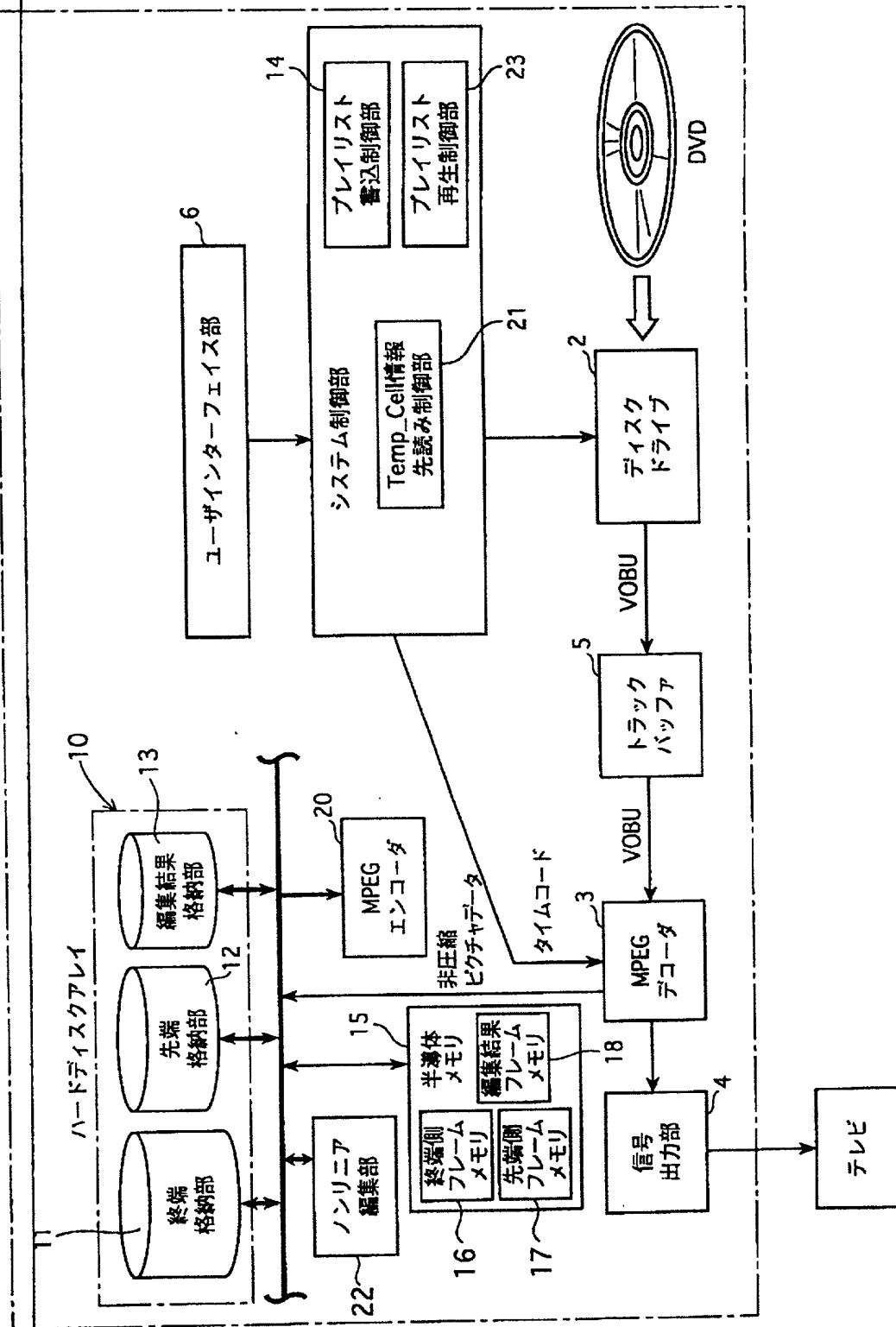


図27

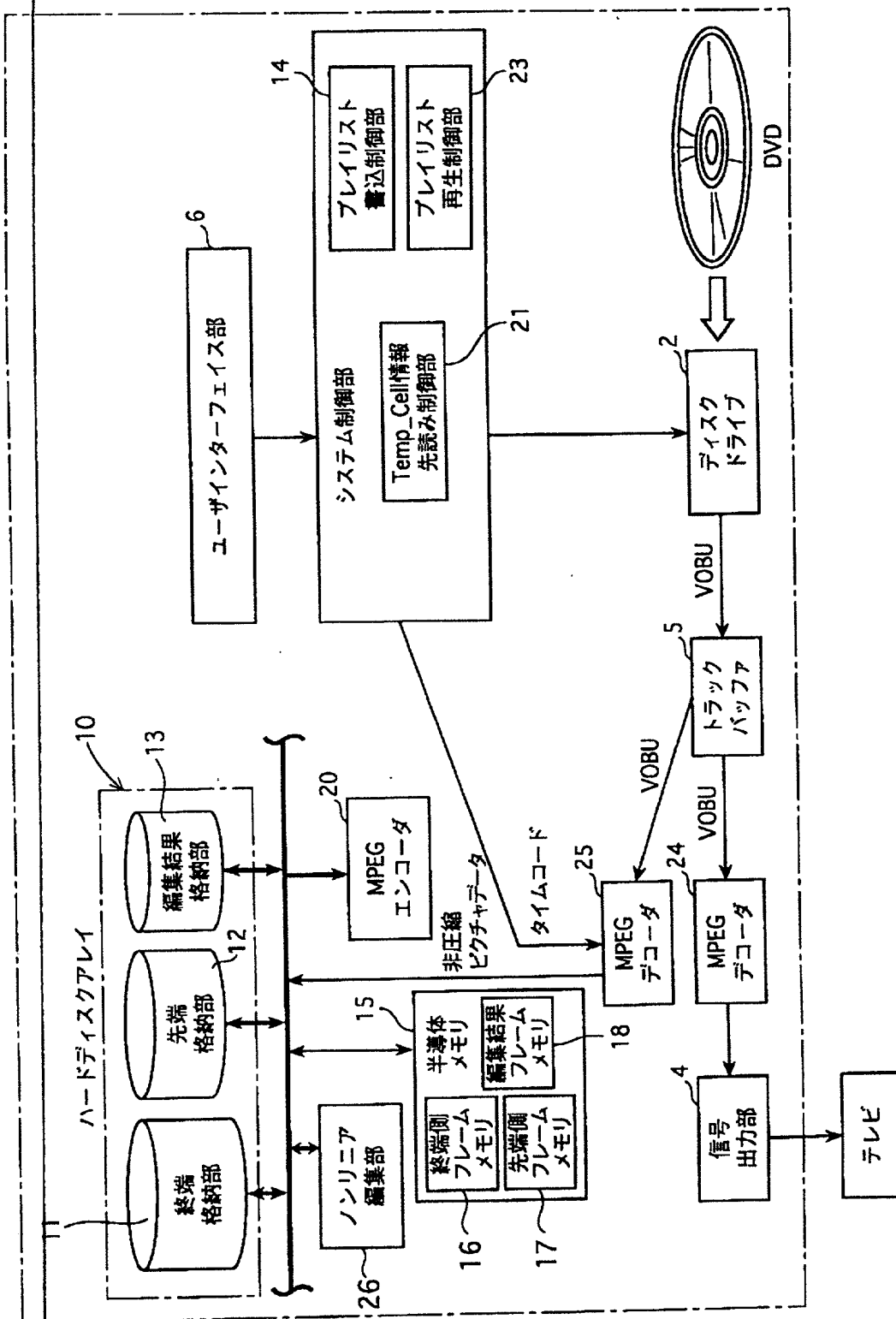


図28

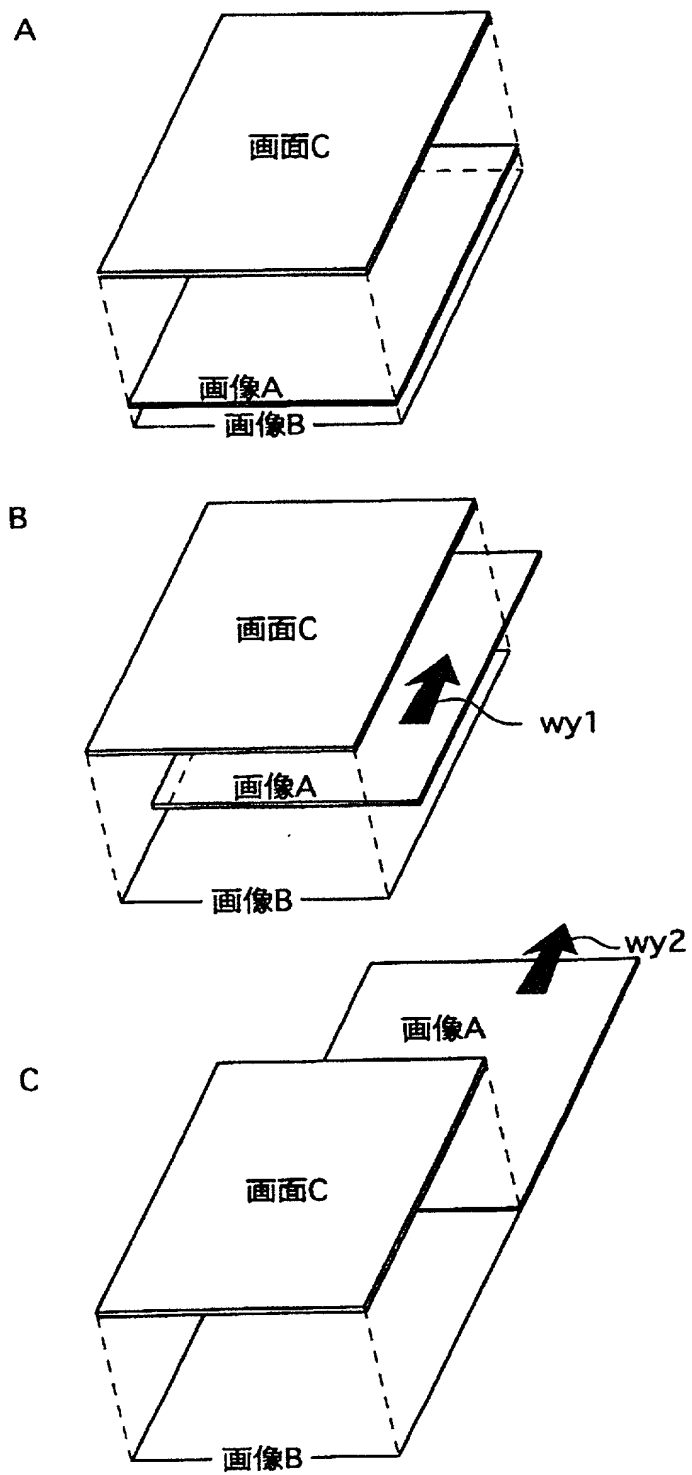


図29

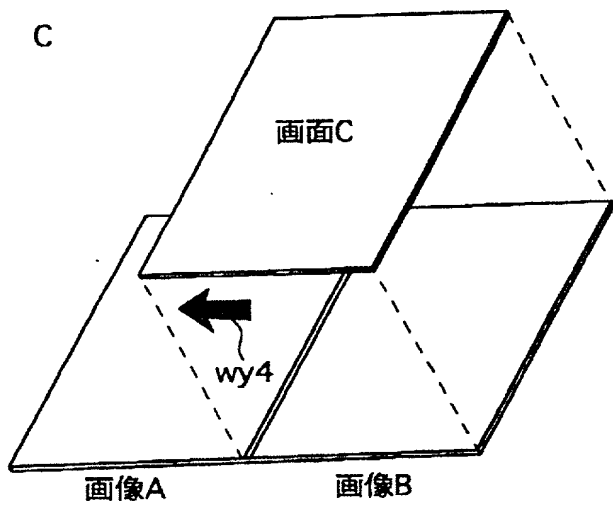
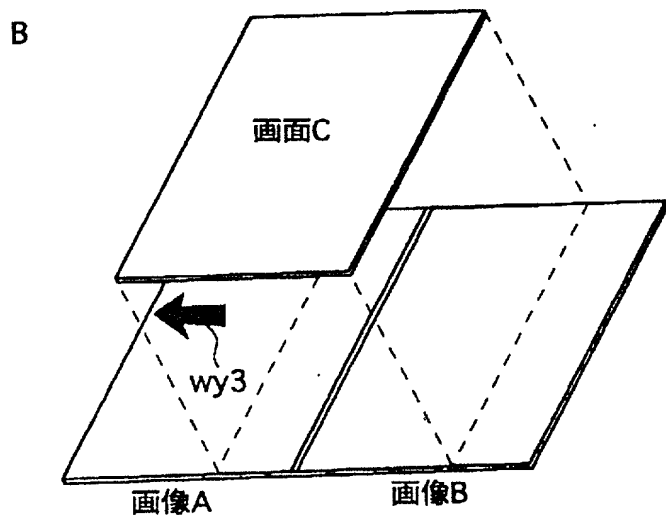
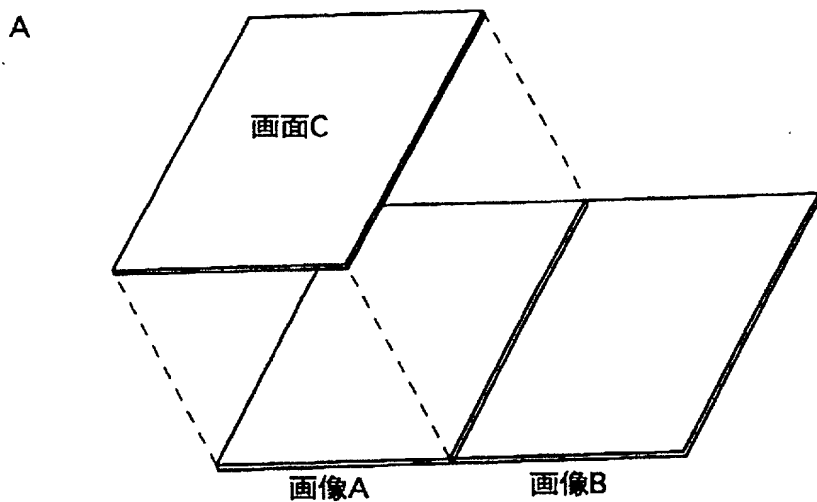


図30

